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#2 4-10-01  
IDS w/refs  
10205.028  
PATENT

10205.028  
PATENT  
JCE60 U.S. PRO  
09/769564  
01/25/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: : Art Unit:  
Story et al. : Examiner:  
Serial No.: : Date: January 25, 2001  
Filed: January 25, 2001 :  
For: NARROW BAND SHADOW ENCODER

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING  
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Date of Deposit

Paul F. Wille

Signature

1-25-01

Date

DISCLOSURE STATEMENT UNDER RULE 56

Honorable Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

SIR:

It is respectfully requested that the art listed below be considered in the  
examination of the subject application and made of record therein. A copy of the  
art listed below is enclosed.

4,748,663 . . . . . discloses (FIG. 1) complementary comb filters 2,3  
Phillips et al. and frequency shift circuit 4  
May 31, 1988

5,386,465 . . . . . discloses (FIG. 2) complementary comb filters 42,  
Addeo et al. 52 and frequency scaler 60;  
January 31, 1995

5,455,543 . . . . . technical background; modulator/ multipliers  
Kechkaylo  
October 3, 1995

5,455,544 . . . . . technical background; modulator/ multipliers  
Kechkaylo  
October 3, 1995

Brennan, Paul V., *Phase-Locked Loops:* technical background; discloses multiplier circuit in  
*Principles and Practice*, R.R. Donnelley & FIG. 2.2;  
Sons Company (1996), pages 8-9

It is respectfully submitted that the invention as described and claimed is not  
disclosed or suggested by the prior art listed above.

Respectfully submitted,



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INFORMATION DISCLOSURE CITATION	Attorney Docket No. 10205.028	Group
	Applicant Story et al.	Serial No.
		Filing Date January 25, 2001

## U.S. PATENT DOCUMENTS

Init.	Document Number	Date	Name	Class	Sub-class	Filing Date
Rs	4748663	5-88	Phillips et al.	379	388	
Rs	5386465	1-95	Addeo et al.	379	202	
Rs	5455543	10-95	Kechkaylo	332	103	
Rs	5455544	10-95	Kechkaylo	332	103	

## FOREIGN PATENT DOCUMENTS

Document Number	Date	Name	Class	Sub-class	Translation

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Rs					Brennan, Paul V., <i>Phase-Locked Loops: Principles and Practice</i> , R.R. Donnelley & Sons Company (1996), pages 8-9

EXAMINER

Ram Ch

DATE CONSIDERED

7/19/02

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

U.S. PTO  
09/769564  
01/25/01